EV085432242

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial	No	09/943,180 C	1237
Filing Date	<u> </u>	August 29, 2001	ᢖ᠘ᢍᡗ
Inventorship		John T. Moore et al. 🕻	•
Assignee	3 APR 1 7 2000 5	Micron Technology, Inc.	
Group Art Unit		John T. Moore et alMicron Technology, Inc2825	
Examiner		Renzo Rocchegiani MI22-1776	
Attorney's Docket No	ADEMARK OF THE	MI22-1776	
		Methods of Forming Capacitors	

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References - See Attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. Copies of the cited art are attached hereto. No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed within three months of the filing date of the application or before the mailing of a first Office Action on the merits, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee Specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted,

Dated: 4-17-03

Mark S. Matkin Reg. No. 32,268